

## **Faculty CV**

1. Name of Faculty : Pankaj Murlidhar Raut
2. Present Position : Lecturer in Electrical Engineering
3. Address : Govt. Polytechnic, Osmanpura, Aurangabad-431 005
4. E-Mail ID : pankajraut8@gmail.com
5. Date of Birth : 29/11/1991

### **QUALIFICATIONS :**

Sr.No.	Examination	Institute	Board/ University	CGPA	Class Award
1	B.E	GEC Aurangabad	Dr. B. A. M. University	6.99	First Class
2	M.Tech (Integrated Power System)	VNIT, Nagpur	VNIT Nagpur	7.85	First Class

### **CAREER DETAILS :**

Sr. No.	Organization	Designation	Duration	Period
1	G P Aurangabad	Lecturer Electrical Engineering	27/07/2016 to Present	3 Years

### **SPECIALIZED TRAINING COURSES ATTENDED:**

Sr. No.	Course Name	Duration	Place	Training Agency
1	Induction Training Programme Phase-I	2 Weeks	MIT Polytechnic Aurangabad	NITTTR Bhopal
2	Wireless Networks through ICT	1 Week	GP Aurangabad	NITTTR Chandigarh
3	Foundation Training Programme	6 Weeks	YASHADA, Pune	YASHADA

## RESEARCH ACTIVITIES:

Presentations and Publications in Referred International Conference Proceedings (Total: 1)

- 1) Error Compensation Method for current transformer in IEEE ICPEICES

## PROJECT GUIDANCE:

Sr. No	Name of Project	No. of Student	Remark
1	Microcontroller based Transformer Testing	06	Won 1 <sup>st</sup> prize in Visio Polytech project competition organized by MGM Polytechnic Aurangabad Got selected in DIPEX Competition
2	Working Model of Universal Motor	06	-----
3	Microcontroller based Current transformer Testing	06	-----

## WORK DONE AT ORGANIZATIONAL LEVEL

- i) Working as a Course Registration Coordinator at departmental level
- ii) Working as NBA Co-coordinator at departmental Level
- iii) Worked as Progressive Test Coordinator at department Level
- iv) Working as a Coordinator for Curricular and Extracurricular Activities, Expert Lectures, and Industrial Visit etc.
- v) Worked as Committee member in First Year Admission (ARC), RAC.

## SIGNATURE