

# Self-Profile



## QUALIFICATION

**Dr. Sachindra B. Dhoot**

B.E, M.E. (Electronics), LMISTE, LMIETE, Ph.D  
Government Polytechnic, Aurangabad (M.S.)

**E-mail** : sbd\_iste@rediffmail.com

**Contact No.:** +91-9405117888 (P).

### Self Objective:

**Selected through MPSC** board in Higher and Technical Education Department of Maharashtra State in **Class-1(c) cadre in 1995**. Since from last 31 years i.e. 1987 to till date working in industrial, academic and research field.

### Personal Details:

<b>Full Name</b>	Dr. Sachindra Badrinarayan Dhoot	
<b>Address</b>	<b>Correspondence Address</b>	<b>Permanent Address</b>
	Dr.S.B.Dhoot Plot No. 63, Pushpak Residency Aditya Nagar, Garkheda Area, Near Laxmi-Narayan Mangal Karyalaya, Aurangabad 431005 (Maharashtra State)	Dr. S.B.Dhoot C/o. Prof.B.C.Dhoot “Champa-Kunj” Darga Road, Near Gangaputra Colony Parbhani-431401(M.S.)
<b>Date of Birth</b>	15 <sup>th</sup> August 1966	
<b>Sex</b>	Male	
<b>Height</b>	186 cm	
<b>Linguistic Proficiency</b>	English, Hindi, Marathi and Rajasthani	
<b>Martial Status</b>	Married	

## **WORK DESCRIPTION [Part-A]**

### **1. Academic Performance:**

<b>Examination</b>	<b>Board / University</b>	<b>Year</b>	<b>Grade</b>
Ph.D.	JNVU Jodhpur (Rajasthan) under QIP (Poly)	2009	
M.E. (E.T.)	Dr. B.A.M.U. Aurangabad	2000	1 <sup>st</sup> Class
B.E. (E.T.)	Amravati University	1987	1 <sup>st</sup> Class
S.S.C.	Aurangabad Board	1981	1 <sup>st</sup> Class

### **2. Professional Experience: 33 Years and above ( Details are given below)**

**Teaching:-** 29 Years and above

**Working as Class-1 Gazetted Officer** with Higher and Technical Education Department of Maharashtra State. Presently working as a Senior Professor in Electronics and Telecommunication Department at Government Polytechnic Aurangabad.

**Industrial:-** 04 Years

Worked with Videocon International Ltd. and Sea Wind Tubes Private Ltd, as a Quality control engineer.

I. As Production Engineer	2 and 1/2 Years
II. As a Quality Control Engineer	1 and 1/2 Years

## **WORK DESCRIPTION [Part-B]**

### **Academic:**

- Working as a Academic co-coordinator of Electronics Department at Government Polytechnic Aurangabad.
- Worked as a Main Coordinator for First Year polytechnic and Pharmacy Admission at G P Aurangabad.
- Worked as a Head of Electronics Department at Government Polytechnic Jintur
- Worked as a Assistant Secretary (RBTE) Aurangabad
- Working as a Research Guide at JNU Jodhpur (Rajasthan)
- Worked as a Head of Computer Dept at G.P.Jalna

- Worked as a Chairman for Direct Second Year Admission at G P Jalna.
- Worked as a Chairman for AICTE Approval Process for 2012 and 2013 at G P Jalna
- Worked as a Coordinator for ISTE Student Chapter at G.P. Aurangabad
- Worked as a Assistant Project officer, in GPA for Community Polytechnic (It is a scheme of central Government of India)
- Worked as a Coordinator for PLC and Industrial Automation training programme.
- Worked as a coordinator for Non-Exam Course in GPA.
- Worked as a **Reporter of the session in National Seminar** on “Recent Advances in Computational Science and Engineering” organized by Computer Engineering Board and Institute of Engineer (India), on 8<sup>th</sup> December 2008, at Jodhpur (Rajasthan).
- Worked as a **Controller of Examination** for MSCIT, **Officer In-Charge of Board Examination, Member of inspection / verification Committee, Member of Discipline Committee, Member of Internal / External Squad and Member of Internal / External Academic Monitoring Committee** etc. at Osmanabad and Aurangabad.
- Worked as an **In-Charge Head of Electronics and Telecommunication Department** (ETC) and Industrial Electronics Department (IE) at Government Polytechnic, Aurangabad for the period of 08-07-2005 to 03-07-2007.
- Worked as a **Coordinator for State Level Paper Presentation Competition** “e-tycoon 2007” organized by Maharashtra State Board of Technical Education, Mumbai on 7<sup>th</sup> March 2007.
- Worked as a **Project Coordinator to prepare the laboratory manual** of two subjects, Power Electronics and Digital Electronics, for Maharashtra State Board of Technical Education (MSBTE) during 2006-2007.
- Worked as an **examiner for P.G. Project** at Dr. Babasaheb Ambedkar Technological University, Lonere.
- Worked as a **Coordinator for one-day workshop on “Microcontroller System” and “poster exhibition on SATCOM”** during 20-24 February 2006.
- Worked as a **member of Jury for National Level Paper Presentation Competition** at Government Engineering College Aurangabad in March 2006.
- Worked as a **Convener for ISTE Approved two weeks National Level Training Program** in August 2006.
- Worked as a **Coordinator for State Level ISTE Convention** in December 2006.
- Worked as a **Coordinator, Program wise Committee of Electronics & Telecommunication and Industrial Electronics Branch** and played active role in Development of new Curricula in 2005-2006.

- Worked as coordinator of EDC cell at GPA.
- Worked as a **Session Chairperson for the National Conference** on Emerging Trends in Electronics,"NCAETE-05" on 11-02-2005 at Osmanabad.
- Worked as an **In-Charge Head of Electronic Department** at Government Polytechnic, Osmanabad during 09-10-1995 to 07-07-2005.
- Organized number of **Expert Lectures and Industrial Visits** for the overall development of the student.
- **Invited lectures delivered** in DIC on "Scope in Electronics" during 2000 and 2001.
- **Selected through MPSC** board in Higher and Technical Education Department of Maharashtra State in **Class-1(c) cadre** and posted at Government Polytechnic Osmanabad as a lecturer in Electronics in September 1995.

## **KEY DIFFERENTIATOR/UNIQUENESS**

### **Awards and Achievements at a Glance:**

#### **A. Significant:**

- **Awarded "Bharat Vidya Shiromani Awards"** for an Excellent academic performance in technical education by Indian Solidarity Council, New Delhi.
- Awarded "**Best Polytechnic Teacher Award for Maharashtra State for 2011**" for an outstanding academic performance in technical education by **Engineering Education Foundation. Pune.**
- Awarded "**ISTE Best Polytechnic Teacher Award for Maharashtra and Goa State for 2010**" for an outstanding academic performance in technical education by **Indian Society for Technical Education, New Delhi.**
- Awarded "**Maharashtra Jeevan Gaurav Purskar**" of state level by **Ekta Sevabhavi Sanstha** for an outstanding academic performance in technical education May 2010.
- Awarded "**Man of the Month Award**" at G.P.Jalna in Jan-Feb 2012 for the outstanding contribution at Institutional Level work
- Awarded "**Certificate of Honour**" by Higher and Technical Education Department for an "Outstanding Academic Performance" in 2009-2010.
- Awarded "**Certificate of Honour**" by Higher and Technical Education Department for an "Outstanding Academic Performance" in 2008-2009.
- Awarded "**Certificate of Honour**" by Higher and Technical Education Department for an "Outstanding Academic Performance" in 2007-2008.
- Selected for Doctoral Program (Ph.D.) under QIP (Poly) Scheme, AICTE, New Delhi at M.B.M. Engineering College, Faculty of Engineering, JNVU Jodhpur (Rajasthan)

and completed the Ph.D. on 31-12-2009.

- **Provisional certificate of Doctoral degree** is awarded by JNVU Jodhpur (Rajasthan).
- Worked as a technical member of State Level Consultancy Project Team for Jodhpur Vidyut Vitran Nigam Ltd., Jodhpur (Rajasthan).
- Awarded “**Certificate of Honour**” by Higher and Technical Education Department for an “Outstanding Academic Performance” in 2006-2007.
- Awarded “**Certificate of Honour**” by The Director, Maharashtra State Science Education Institute for doing a work as an examiner in 27<sup>th</sup> State Level Science Exhibition during 17-21 January 2002.
- Awarded “**Certificate of Honour**” by Higher and Technical Education Department for an “Outstanding Academic Performance” in 1998-1999.
- Awarded by “**Certificate of Honour**” for excellent performance in state level technical project competition from 1998 to 2004.
- Awarded “**Outstanding Academic Performance**” remark by MIT Engineering College, Aurangabad, in Performance appraisal for the year 1994-1995.
- Awarded “**Outstanding Academic Performance**” remark by MIT Engineering College, Aurangabad, in Performance appraisal for the year 1993-1994.
- Awarded “**Certificate of Honour**” by Higher and Technical Education Department for an “Outstanding Academic Performance” in 2006-2007.
- Awarded by “**Certificate of Merit**” for excellent performance in SSC examination on 15<sup>th</sup> August 1981.

### **CONTRIBUTION TO THE SOCIETY**

- Worked as a **Chief Coordinator for Programmable Logic Controller (PLC) based short term training program** and generate the revenue (more than Three Lakhs Rupees) for the Department and Institute. The program was designed as per the need of Industry.
- **Organized cultural and social activities** during school and college level which enhanced my capabilities of designing and developing assignments.
- **Worked as a Vice President of Gymkhana** (Department of extra-curricular activity) from 1996 to August 2004 at Government Polytechnic, Osmanabad.
- **As a Chief Editor, first Annual Student Magazine, “SHABDAMANTHAN”,** of Government Polytechnic Osmanabad, was published in 1998 and worked successively for three years i.e. from 1998-2001.
- Since from last five years, organizing the mega event of THINK-QUEST WIZARD TALENT SEARCH PROGRAMM for the student.. Question bank of 3800 question with answer has been prepared.

## **VISION FOR THE FUTURE**

- My objective is to excel in anticipating and quickly responding to mettlesome and challenging assignments as an individual as well as in group and thus ensuring my credibility towards the outer world. At this junction, now trying to accelerate the visibility to implement new innovative ideas in my working era for better result.
- A person is ready to learn, share, experiments and loves to take up challenges and accomplish them. I always consider mistakes as a part of learning process and my proactive approach helps me to learn from them.

## **PARTICIPATION IN ACTIVITIES**

### **Professional :**

- Worked as a coordinator and in charge for establishing laboratory for Electronics Meter Testing Lab for Jalna district.
- The project guided and implemented has got the participation award of Rs. 11000 in international exhibition “ELECARAMA-06” at Mumbai in January 2006. It was the only project selected from all government polytechnic of Maharashtra State.  
The title of project was:-
  - ▶ **Microcontroller based Glass Breakage System.**
- ⇒ The project designed, guided and implemented has won the First as well as Second prize and General Championship in State Level Project Competition, DIPEX- 2003, at Dhule (M.S.). The title of projects were:-
  - ▶ **Multifunctional Microcontroller based Petrol Sensor. (1st Prize)**
  - ▶ **Cable Connection Prepaid Card. (2nd Prize)**
- ⇒ The project guided entitled “**Auto Shuttering**” has won the First Prize in State Level Project Competition, DIPEX- 2002, at Nagpur (M.S.).
- ⇒ The project guided entitled “**Telecontrolled Robotic Fighter**” has won the First Prize in State Level Project Competition, DIPEX- 2000, at Kolhapur (M.S.).
- ⇒ Worked as a coordinator for AICTE approval process for 2012 and 2013.
- ⇒ Delivered two expert lectures at G.P.Ambad in 2011-2012.
- ⇒ Delivered two expert lectures at G.P.Aurangabad in 2013.
- ⇒ Delivered one expert lectures at G.P. Jalna in 2013.

- Adhar Card project with innovative modification has been implemented by Student. This idea was implemented by arranging the competition as per the instruction given by the collector office Jalna. Following student has taken the effort under the guidance of Dr.S.B.Dhoot, Head CO Department.
  - Navin balla
  - Sayyed sharukh
  - Pratik Agrawal
  
- Projected a model of “**Picture Tube Tester**” in MAHAEXPO - 1992 at Aurangabad.

### **Research:**

- Research Proposal on “**Development of Fault Identification Trainer**” is accepted by Department of Science and Technology (DST), Rajasthan State Government and releases the Grant for the year of 2009-2010. The project has been completed successfully and report submitted to the DST on 31<sup>st</sup> March 2010.
- Innovative Research Proposal on “**Intelligent Fault Diagnosis of Industrial System**” has been sanctioned by Department of Science and Technology (DST), Rajasthan State Government and releases the Grant for the year of 2008-2009. The project has been completed successfully and report submitted to the DST on 31<sup>st</sup> March 2009.
- Research Proposal on “**Intelligent Fault Diagnosis of Electronics Circuit**” is submitted to the Institute of Engineer (India) on 31<sup>st</sup> March 2009 for Research Grant, to carry further development and proposal is under process.
- **Two papers are submitted to the National Journal** in 2012 and published in March 2012 (List is enclosed in Annexure Two).
- **Five papers are submitted to the International and National Journal** (List is enclosed in Annexure Two).
- **Five papers were published in the International Conference** in the year 2007, 2008, 2014 and one will be in Jan 2015.(Accepted) (List is enclosed in Annexure)
- **Eight papers were published in the National Conference** in the year 2004, 2005, 2006, 2007, and 2008. (List is enclosed in Annexure)
- **Two papers were published in the National Convention of Institute of Engineers (IEI)** in the year 2008 and 2009. (List is enclosed in Annexure)
- Worked as a coordinator to submit the proposal under **MODROBS for development of EMBEDDED SYSTEMS LAB** in 2006-2007 and received Grant of **Rs.6 Lakhs** from AICTE. ( Project was executed by other faculty member after the Selection for Doctoral Program (Ph.D.) under QIP (Poly) Scheme, AICTE, New Delhi in 2007-2008)
- Till date, **more than 100 projects / seminars** are successfully guided in engineering field.

## **Extra Curricular:**

- **Won the First Prize in Chess Tournament** organized by Government polytechnic, Jalna for Government Employee of Marathwada Region during December 2003.
- **Won the Second Prize in Double Wicket Cricket Tournament** organized by Government polytechnic, Jalna for Government Employee of Marathwada Region during December 2003..
- **Winner in State level Hindi Debate Competition** in 1982 and 1983 successively.
- **Winner in the event of Chess Tournament** conducted by Parbhani District Chess Association on 27<sup>th</sup> January 1982.
- **Won the Third Prize in Chess Tournament** conducted by Parbhani District Chess Association on 26<sup>th</sup> January 1978.
- **Won the Third Prize in “Sahitya Bodh” Examination** On 10 July, 1976.

## **Work Experience:**

### **July 2016 to till date**

- Working as a Sr. Professor at G P Aurangabad.

### **August 2014 to July 2016**

- Worked as a I/C Head of Electronics Department at Government Polytechnic Jintur

### **November 2013 to July 2014**

- Worked with Government Polytechnic, Ambad. ( On Deputation)

### **August 2013 to November 2013**

- Worked as a Assistant Secretary (RBTE) Aurangabad.

### **Oct. 2012 to August 2013**

- Worked as a I/C of **Electronics Department** with Government polytechnic Jalna

### **July 2011 to Sept 2012**

- Worked as a I/C **Head of the Computer Department** of G.P. Jalna

### **June 2010 to July 2011**

- Worked with Government polytechnic Aurangabad

### **July 2007 to June 2010**

- Selected for three years Doctoral Program (Ph.D.) under QIP (Poly) Scheme, AICTE, New Delhi in 2006 & Completed research study in the field of “Intelligent Fault Diagnosis” at M.B.M. Engineering College, Faculty of Engineering, JNVU Jodhpur (Rajasthan)



**July 2005 to June 2007**

- Worked as a **Head of the Department** (I/C) of Electronics and Telecommunication (ETC) and Industrial Electronics (IE) Engineering Department at Government Polytechnic, Aurangabad.

**October 1995 to June 2005**

- Worked as a **Head of the Department** (I/C) of Electronics Engineering Department at Government Polytechnic, Osmanabad.

**August 1991 to September 1995**

- Worked as a Lecturer in MIT College of Engineering, Aurangabad. Selected by UGC committee.

**February 1990 to July 1991**

- Worked as a Quality Control Engineer in Sea Wind Tubes Pvt Limited industry at Aurangabad.

**August 1987 to January 1990**

- Worked as an Engineer in Videocon International Limited at Ahmednagar and Aurangabad.

**Research / Project / Seminar Experience:****Doctoral Program Level**

- Research work in the field of “Intelligent fault Diagnosis techniques for Electronics circuit” under QIP (Poly) scheme has been completed.

**Post Graduate Program Level**

- Delivered seminar on “ 3-D Television”
- Implemented Project entitled “Improved Economy Television System”, sponsored by Matrix Electronics. Innovative solutions were developed for several problems in television system as per the current need of industry.

**Graduate Program Level**

- Delivered seminar on” Static over Current Relay”.
- Project titled” Temperature Measurement System” developed as per the curriculum.

**Educational Learning Skill:**

- Accelerating the visibility to learn innovative technical aspect at Ph.D. level.
- Got the proficiency in electronics and telecommunication field and developed skills at M.E. level.
- Instilled discipline, confidence, technical and competitive attitude developed at B.E. level.
- Analytical and logical ability developed at higher secondary school certificate level.
- Acquired the skill of working in-group at secondary school certificate level.

## Training / Workshop:

- ▣ **Attended the Training Program of one week on “Capacity Building”** at Government Polytechnic Aurangabad ( 10/11/2014 to 14/11/2014)
- ▣ **Attended the workshop of Curriculum Revision Project – 2012** at MSBTE, Mumbai (19/09/2011 to 21/09/2011, 12/10/2011 to 13/10/2011 and 19/12/2011 to 21/12/2011)
- ▣ **Completed one week Content Updating Programme** in the field of Microcontroller and Embedded System during 03-07 January 2011 at G.P. Aurangabad. Programme was sponsored by MSBTE, Maharashtra
- ▣ **Attended 40<sup>nd</sup> National Level Convention of Indian Society for Technical Education (ISTE)** at Mopec Schlenk Engineering College at Sivakasi (Tamilnadu). during 02-04 December 2010.
- ▣ **Completed two weeks Faculty Development Programme (FDP)** in the field of entrepreneurship during 17-29 August 2009 at M.B.M. Engineering College, Jodhpur (Rajasthan). Programme was sponsored by DST, New Delhi.
- ▣ **Attended the workshop on “MATLAB: The Language of Technical Computing”** held on 26-27 December 2008 at Vyas Engineering College for Girls, Jodhpur (Rajasthan).
- ▣ **Attended the International Workshop on “Vibration and Noise: Issues and Challenges”** held on 17<sup>th</sup> December 2008. This workshop was sponsored by European Commission and Swedish International Development Cooperation Agency, Stockholm.
- ▣ **Passed GECT (Computer training for Government Employees) examination** which was conducted by M.S.B.T.E. Mumbai and secured 91% marks.
- ▣ **Completed four weeks training** on “Computer Application” at Government polytechnic (Girls), Latur during June 2001. Course was approved by ISTE, New Delhi.
- ▣ **Attended 2<sup>nd</sup> State Level Convention of Indian Society for Technical Education (ISTE)** at J.N.E.C. Aurangabad during 16-17 January 1998.
- ▣ Participated in **one week training program of Computer Hardware and Maintenance** at Government polytechnic, Aurangabad in October 1998.
- ▣ Completed **four week training of Induction Phase-One under Staff Development Program** at Government polytechnic, Aurangabad during June-July 1997.
- ▣ **Participated in state level seminar** on “Reprogramming of Computer Education in Engineering and Polytechnic” at Center for Development of Advance Computing (CDAC), Pune during 10-11 August 1996.
- ▣ **Completed the training program on Electronics Circuit and PCB Designing** during 07-09<sup>th</sup> August 1996 at Mumbai. The training program was organized by software option (I) Limited.
- ▣ **Attended L.R.D.C. workshop** on at Saint Xavier Technical Institute, Mumbai on 29<sup>th</sup> November 1995.

### Membership:

- ▣ **Life member of Indian Society for Technical Education (ISTE)**, New Delhi, since 1992. ISTE is working to improve the quality and standard in technical education.
- ▣ **Associate member of Institute of Electronics and Telecommunication Engineering (IETE)**, New Delhi, since 1992.

### Interests:

- ◆ **Traveling** - A passionate traveler to the wood and country, which help me in achieving the peace of mind and activate more responsibility.
- ◆ **Reading** – An intense reader of a spiritual literature and technical information which prosper my knowledge in each field.

31 December 2019

Place: Aurangabad

**Dr. Sachindra B. Dhoot**

## **Annexure [Part-B]**

### **A] Sanctioned and Completed Research Project of Department of Science and Technology (DST) of Rajasthan State Government**

- Research Proposal on “**Development of Fault Identification Trainer**” is accepted by Department of Science and Technology (DST), Rajasthan State Government and releases the Grant for the year of 2009-2010. The project has been completed successfully and report submitted to the DST on 31<sup>st</sup> March 2010.
- Innovative Research Proposal on “**Intelligent Fault Diagnosis of Industrial System**” has been sanctioned by Department of Science and Technology (DST), Rajasthan State Government and releases the Grant for the year of 2008-2009. The project has been completed successfully and report submitted to the DST on 31<sup>st</sup> March 2009.

**B] At present, *one research scholar* is doing research under my guidance at Jodhpur National University (Deemed University)**

**C] Research papers presented / published in Journals / Seminar / Symposia / International and National Conferences:**

Details are given in reverse chronological order:-

<b>Sr. No</b>	<b>Title of the paper</b>	<b>Authors</b>	<b>Name of the Conference</b>	<b>Year</b>
<b>01</b>	Review Paper on Hand Based Gesture Recognition Technology	Dr S B Dhoot, Rutvik P Kshirsagar	ISSN NO 2395/0056 International Research Journal of Engineering and Technology (IRJET), Vol06, June 2019	June 2019
<b>02</b>	Artificial Intelligence based smart ATM	Dr S B Dhoot, Ankita Mahajan, Radhika Mande, Avinash Mhaske, Parth Shete	ISSN NO 2395/0056 International Research Journal of Engineering and Technology (IRJET), Vol06, Nov 2019	Nov 2019
<b>03</b>	Automatic Transmission for the Geared Bikes	Dr S B Dhoot, Dhananjay Patil, Hemant Patil, Krishna Pandit	ISSN NO 2395/0056 International Research Journal of Engineering and Technology (IRJET), Vol 06, issue 11, Nov 2019	Nov 2019
<b>04</b>	Design of Compact Multiband Fractal Antenna for WLAN and WiMAX Applications	Dr. Sachindra Dhoot, Kantilal Kharat, Dr. (Mrs.)Jayshri Vajpai	International Conference on Pervasive Computing ( <b>ICPC 2015</b> ) Advanced Communication Technology and Application for Society.	08-10 Jan. 2015
<b>05</b>	Design & Analysis of Microstrip Fractal Array Antenna with high gain for Wireless Applications	Dr. Sachindra Dhoot, Kantilal Kharat, Dr. (Mrs.)Jayshri Vajpai	10th International Conference on Microwaves, Antenna, Propagation and Remote Sensing, <b>ICMARS-2014, Jodhpur, INDIA.</b>	23 rd December 2014
<b>06</b>	Artificial Neural Network Based Fault Diagnosis in Analog Circuit	Dr.Sachindra Dhoot Dr.Ulhas Shiurkar	ISSN NO 2249/7455 Journal of Advances in Management Technology and Engineering Science.	March 2012

<b>07</b>	An Overview of Application Software Packages for Electronics Circuits Design and Simulation	Dr.Sachindra Dhoot Dr.Ulhas Shiurkar	ISSN NO2249 /7455 International Journal of Business, Management and Social Science.	March 2012
<b>08</b>	Artificial Neural Network based Intelligent Fault Diagnosis in Analog Circuits	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	Second International Conference on Advances in Computer Vision and Information Technology (ACVIT 2009)	16-19 Dec. 2009
<b>09</b>	Probabilistic Intelligent Fault Diagnosis in Television Receiver Circuit using Visual Symptoms	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	Published in International Journal of Applied Engineering Research Vol. 4, no. 8, pp. 1535–1542, 2009.	2009.
<b>10</b>	Application Software Packages and Simulation	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	Manuscript submitted to the Institute of Engineer (India), for Electronics and Telecommunication Engineering journal.	01 May 2009.
<b>11</b>	HDTV: A Future Trend in Television Receiver	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	Emerging Trends in Electronics and Telecommunication: A Vision 2020 of Institute of Engineers(India)	07-08 Marc. 2009
<b>12</b>	Probabilistic Computing: A Practical approach for Intelligent Fault Diagnosis	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	23 rd National Convention of Computer Engineers of Institute of Engineers(India)	07-08 Dec. 2008
<b>13</b>	Probabilistic Techniques for Intelligent Fault Diagnosis in Complex System	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	International Conference on Emerging Techniques in Computing, Electronics and VLSI Design	20-21 Marc. 2008
<b>14</b>	Intelligent Fault Diagnosis: Future Trend in Automation	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	National Conference on Futuristic Trends in Emerging and Technology( FTET-08)	28-29 Jan. 2008
<b>15</b>	Intelligent Fault Diagnosis: State of Art & Application	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	IEEE Sponsored International Conference on Recent Application in Soft Computing.	22-23 Dec. 2007
<b>16</b>	Probabilistic Techniques for Intelligent Fault Diagnosis in Complex System	Dr.(Mrs.) Jayashri Vajpai and Sachindra Dhoot	Vijnana Parishad of India's 12 <sup>TH</sup> Annual Conference and National Symposium.	25-27 Oct. 2007

<b>17</b>	Technical Education in Changing Global Scenario	Sachindra Dhoot	XI State Convention of ISTE	02-03 Dec. 2006
<b>18</b>	Optical Fiber TX Network	Sachindra Dhoot and Miss S.J.Meshram	A National Conference: TSyGECA-06	23-24 Jan. 2006
<b>19</b>	Embedded Digital Vehicle System	Sachindra Dhoot, Mrs. S.S.Agrawal and Mrs.V.G. Mahendra	A National Conference: TSyGECA-06	23-24 Jan. 2006
<b>20</b>	SONNET	Sachindra Dhoot and D.D.Khumane	A National Conference on Advances in Electronics and telecommunication (NCAETE-05)	25-26 Feb. 2005
<b>21</b>	Advances in the field of Telecommunication	Sachindra Dhoot and Miss S.J.Meshram	A National Conference on Advances in Electronics and telecommunication (NCAETE-05).	25-26 Feb. 2005
<b>22</b>	Robot Kinematics	Sachindra Dhoot and D.D.Khumane	A National Conference on Emerging Trends in Mechatronics	23-24 Jul. 2004